



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,918	12/03/2001	Neil Gibson	TI-32968	7227

7590 08/07/2003

Dan Swayze  
Texas Instruments Incorporated  
P.O. Box 655474  
M/S 3999  
Dallas, TX 75265

EXAMINER

NGUYEN, LINH V

ART UNIT PAPER NUMBER

2819

DATE MAILED: 08/07/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/008,918	GIBSON ET AL.	
	Examiner Linh V Nguyen	Art Unit 2819	
<i>-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --</i>			
<b>Period for Reply</b>			
<b>A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.</b>			
<ul style="list-style-type: none"> <li>- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.</li> <li>- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.</li> <li>- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.</li> <li>- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).</li> <li>- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>			
<b>Status</b>			
1) <input checked="" type="checkbox"/> Responsive to communication(s) filed on <u>23 June 2003</u> .			
2a) <input type="checkbox"/> This action is FINAL.                    2b) <input checked="" type="checkbox"/> This action is non-final.			
3) <input type="checkbox"/> Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.			
<b>Disposition of Claims</b>			
4) <input checked="" type="checkbox"/> Claim(s) <u>1,3-8,10-22</u> is/are pending in the application.			
4a) Of the above claim(s) _____ is/are withdrawn from consideration.			
5) <input checked="" type="checkbox"/> Claim(s) <u>10-19</u> is/are allowed.			
6) <input checked="" type="checkbox"/> Claim(s) <u>1,3-8,20-22</u> is/are rejected.			
7) <input type="checkbox"/> Claim(s) _____ is/are objected to.			
8) <input type="checkbox"/> Claim(s) _____ are subject to restriction and/or election requirement.			
<b>Application Papers</b>			
9) <input type="checkbox"/> The specification is objected to by the Examiner.			
10) <input checked="" type="checkbox"/> The drawing(s) filed on <u>03 December 2001</u> is/are: a) <input checked="" type="checkbox"/> accepted or b) <input type="checkbox"/> objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).			
11) <input type="checkbox"/> The proposed drawing correction filed on _____ is: a) <input type="checkbox"/> approved b) <input type="checkbox"/> disapproved by the Examiner. If approved, corrected drawings are required in reply to this Office action.			
12) <input type="checkbox"/> The oath or declaration is objected to by the Examiner.			
<b>Priority under 35 U.S.C. §§ 119 and 120</b>			
13) <input type="checkbox"/> Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) <input type="checkbox"/> All    b) <input type="checkbox"/> Some * c) <input type="checkbox"/> None of: 1. <input type="checkbox"/> Certified copies of the priority documents have been received. 2. <input type="checkbox"/> Certified copies of the priority documents have been received in Application No. _____. 3. <input type="checkbox"/> Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.			
14) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application). a) <input type="checkbox"/> The translation of the foreign language provisional application has been received.			
15) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.			
<b>Attachment(s)</b>			
1) <input type="checkbox"/> Notice of References Cited (PTO-892)		4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____	
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)		5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)	
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____		6) <input type="checkbox"/> Other: _____	

Art Unit: 2819

***R sponse to Am ndment***

1. This office action is in response to applicant's amendment received on 05/12/03.

Claim 9 has been canceled. Claims 1, 3 – 8, and 10 - 22, are pending on this application.

***Response to Arguments***

2. Regarding to claims 20 and 22, Under the "Remark", applicant argued that Uscategui does not teach the plurality of transistor configured as translinear loop of the claimed invention. Examiner is respectfully traversed from the following:

Applicant's claimed invention fails to distinct the structures and limitation of the translinear loop over the references applied against the claims. Also Applicant needs to disclose or explain how the structures or limitation of the translinear loop of the claim avoid from the references or distinguish from them, and not because of the terminology of translinear loop.

Regarding to applicant's argued that the rejection under 35 U.S.C. 101 is premature since no application in fact patented or allowed. However ,copending application 10005463 has been allowed from the office and in process of issue the invention. Therefore the conflicting claims 1, 3 – 8 , on this application required to cancel.

***Double Patenting***

3. Claims 1, and 3 – 8, of this application conflict with claims 1 - 5 of Application No. 10005463 (has been allowed). 37 CFR 1.78(b) provides that when two or more

Art Unit: 2819

applications filed by the same applicant contain conflicting claims, elimination of such claims from all but one application may be required in the absence of good and sufficient reason for their retention during pendency in more than one application.

Applicant is required to either cancel the conflicting claims from all but one application or maintain a clear line of demarcation between the applications. See MPEP § 822.

4. Claims 1 and 3 - 8, provisionally rejected under 35 U.S.C. 101 as claiming the same invention as that of claims 1 – 5, of copending Application No. 10005463, which has been allowed and in processing for issue the invention.

#### ***Claim Rejections - 35 USC § 102***

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 20 - 22, are rejected under 35 U.S.C. 102(b) as being anticipated by Uscategui et al. U.S. patent No. 5,789,982.

Fig. 2 Uscategui et al. as discussed from previous office action as applied to claims 20 – 22 above disclose every aspect of applicant's claimed invention. (Hence the claimed invention fails to distinct the structures or limitation for configuration of transistors in translinear loop over the reference applied to the claims)

***Allowable Subject Matter***

15. Claims 10 – 19, are allowed.

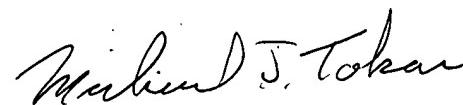
***Contact Information***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh Van Nguyen whose telephone number is (703) 305-1934. The examiner can normally be reached from 8:30 – 5:00 Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Michael Tokar can be reached at (703) 305-3493. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

LVN

August 5, 2003



Michael Tokar  
Supervisory Patent Examiner  
Technology Center 2800